

Applicant(s)	James D. Beasom	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	Unknown 10-851360	
Filing Date	Unknown	
Group Art Unit	Unknown 2326	
Examiner Name	Unassigned C44	
Attorney Docket No.	125.008US02	

Title: INTEGRATED CIRCUIT WITH A MOS STRUCTURE HAVING REDUCED PARASITIC BIPOLAR TRANSISTOR ACTION

Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Patent No.	Issue Date	Name	Filing Date
CAL	4,564,770	01/14/86	Sherman et al.	03/29/83
	4,969,852	11/13/90	Osterwald	09/13/89
	5,138,177	08/11/92	Morgan et al.	03/26/91
	5,698,867	12/16/97	Bauer et al.	03/24/95
	5,821,144	10/13/98	D'Anna et al.	09/29/97
	6,035,235	03/07/00	Perttu et al.	03/30/98
	6,133,107	10/17/00	Menegoli	03/03/99
	6,166,925	12/26/00	Richter et al.	08/06/99
	6,172,398	01/09/01	Hshieh	08/11/97
V	6,239,958	05/29/01	Kato et al.	12/18/98
CAL	6,368,920	04/09/02	Beasom	06/11/98

Foreign Patent References				
Examiner Initials	Foreign Patent	Name	Publication Date	T?
	Country	No.		
CAL	EP	0747958	12/11/1996	
CAL	JP	7-193231	09/25/2001	
CAL	EP	0747966	11/12/1996	
CAL	WO	00/10204	02/24/2000	
CAL	JP	63281468	11/1998	

Other References			
Examiner Initials	Author, Title, Date, Pages, etc.		
CAL	Mizuno et al., Publication No: US 2001/0048119, "Semiconductor Device and Method of Manufacturing The Same", Published 12/06/01, Filed March 19, 2001.		
CAL	Blanchard et al., Publication No. US 2002/0017684, "Transistor with Integrated Photodetector for Conductivity Modulation", Published 02/14/02, Filed 04/27/00.		

Examiner Signature		Date Considered	8/23/04
--------------------	---	-----------------	---------

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce